IAP6 Rec'd PCT/PTO 28 AUG 2006

Docket No.: 2004P00792

CERTIFICATION

I, the below named translator, hereby declare that: my name and post office address are as stated below; that I am knowledgeable in the English and German languages, and that I believe that the attached text is a true and complete translation of PCT/EP2005/050508, filed with the European Patent Office on February 7, 2005.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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PCT/EP2005/050508 / 2004P00792WOUS

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- 1 Method and device for control of a capacitive actuator
- 2 The invention relates to a method and a device for control of a
- 3 piezoelectric actuation element, especially of a piezoelectric
- 4 actuator for an injection valve of an internal combustion
- 5 engine.
- 6 Major demands are made on the control electronics of the
- 7 actuator for the control of capacitive actuators, i.e. for
- 8 charging or discharging capacitive actuators. This means that
- 9 voltages in the range of several 100 V and short-duration
- 10 currents for charging and discharging of more than 10 A must be
- 11 provided. The control is mostly undertaken in fractions of
- 12 milliseconds. At the same time the current and the voltage is
- 13 to be fed to the actuator in a controlled manner during these
- 14 control phases.
- 15 One embodiment of a capacitive actuation element is represented
- 16 by a piezoelectric actuator, as is used to actuate an injection
- 17 valve. Such an injection valve is used in internal combustion
- 18 engines for injecting fuel into a combustion chamber. Very high
- 19 demands are imposed here on an exact and reproducible opening
- 20 and closing of the valves and thereby also on the control of
- 21 the actuator. To enable future exhaust emission limit values to
- 22 be met, the number of fuel injections per combustion phase is
- 23 increased. This means that the injection times and thereby also
- 24 the activation times for a piezoelectric actuator become ever
- 25 shorter, which imposes additional demands on the control
- 26 electronics of the actuator.
- 27 In a known circuit arrangement (DE 199 44 733 Al) a
- 28 piezoelectric actuator is charged by a charge capacitor via a
- 29 transformer. To do this a charging switch arranged on a primary
- 30 side of the transformer is controlled with a pulse-width-
- 31 modulated control signal. The charging switch and also the

- 1 discharging switch are embodied there as controllable
- 2 semiconductor switches. Predefined energy packets are fed to
- 3 the piezoelectric actuator or removed from it, for charging or
- 4 discharging.
- 5 If energy packets are needed which are smaller than the
- 6 predefined energy packets, the known circuit arrangement, for
- 7 averaging out the energy fed to and removed from the
- 8 piezoelectric actuator over time, needs a highly effective
- 9 output filter. Furthermore identical charging and discharging
- 10 currents are required here if the characteristic control curve
- 11 of the actuator is not to exhibit any unevenness.
- 12 The object of the invention is to create a method and a device
- 13 for controlling a capacitive actuator of which the outstanding
- 14 features are a high resolution and reproducibility.
- 15 This object is achieved in accordance with invention by a
- 16 method with the features of claim 1 as well as by a device with
- 17 the features of claim 11.
- 18 In accordance with the inventive method the actuator is charged
- 19 in at least three stages, each with a predefined duration.
- 20 During these at least three periods a current flows into the
- 21 actuator when the actuator is charged up.
- 22 During the first period an amplitude of the current is
- 23 increased from a predefined minimum to a predefinable maximum.
- 24 During the second period the amplitude of the current is kept
- 25 approximately constant. Finally, during the third period, the
- 26 amplitude of the current is reduced from a predefined maximum
- 27 current to a likewise predefined final value.
- 28 The inventive device features a control unit and a final stage,
- 29 with the final stage being controlled via a control signal of
- 30 the control unit. The control unit makes a control signal

- 1 available for a first predefined period which rises during this
- 2 first period from a predefined minimum to a predefined maximum.
- 3 For a second predefined period which follows the first period,
- 4 the control unit makes a largely constant control signal
- 5 available. For a third predefined period the control unit makes
- 6 a control signal available which reduces over the third
- 7 predefined period from the predefined maximum to a predefined
- 8 end value.
- 9 The corresponding assumptions also apply for discharging the
- 10 actuator. In this case the maximum amplitude of the current is
- 11 also increased during the first period from a minimum to a
- 12 maximum. During the second period the amplitude is kept
- 13 constant and during the third period the amplitude of the
- 14 current is reduced from a maximum to an end value which can
- 15 also be predefined. In its case the discharge current is
- 16 directed so that the energy stored in the actuator reduces.
- 17 Through the inventive control of the actuator a soft start and
- 18 end sequence of the electrical charging fed to the actuator is
- 19 achieved since for example, with a piezoelectric actuator, the
- 20 charge being fed to this actuator is proportional to its change
- 21 in travel and force change, a slow change of the charge over
- 22 time in the start and end sequence of the charging or
- 23 discharging prevents overdriving of the actuator. Disruptive
- 24 mechanical or acoustic effects are reduced by this.
- 25 A control of the charge or discharge current is required
- 26 exclusively for the period of the charging or discharging.
- 27 Advantageous developments of the invention are specified in the
- 28 subclaims.
- 29 In the first preferred embodiment it is possible to set the
- 30 charge fed to the actuator without any change in the overall

- 1 charging period. Only the rises in the first and in the third
- 2 period of the charging or discharging are changed here. By
- 3 adapting the rise the linearity of the control can be
- 4 influenced.
- 5 In a further preferred embodiment the amount of charge fed to
- 6 the actuator is varied by a change to the second period. In
- 7 this way, despite quantizing, as is induced for example by a
- 8 clocked final stage, the actuator can be controlled in a linear
- 9 manner. The first and/or third period remain unchanged here,
- 10 which only shifts the power-down ramp in time and the power-
- 11 down behavior remains the same.
- 12 This enables a further linear control range to be achieved. In
- 13 this case changes of between 10 and 100% relation to the energy
- 14 or 20 to 100% in relation to the time are possible. A linearity
- 15 error of less than 0.5% is able to be implemented in this way.
- 16 Different charging and discharging currents have no effect on
- 17 the linearity of the control method.
- 18 In a preferred embodiment the control signal fed to the final
- 19 stage produces a predefined output current. The control signal
- 20 can be created by an analog or a digital circuit. Thus the form
- 21 of the current fed to the actuator can then be set
- 22 independently of the control of the final stage itself.
- 23 Advantageously the maximum amplitude of the current during the
- 24 second period and the maximum of the third period approximately
- 25 correspond to the predefined maximum of the first period.
- 26 In a further preferred exemplary embodiment the enveloping of
- 27 the maximum amplitude over the three predefined periods
- 28 approximately corresponds to the shape of a trapeze.
- 29 In a further preferred embodiment the charging or discharging
- 30 current is an intermittent current which is made available for

- 1 example by a clocked current or voltage source.
- 2 Furthermore the current can be made up of a series of pulses,
- 3 of which the maximum amplitude lies at a predefined point of
- 4 the envelope curve for this period in each case.
- 5 Triangular pulses are advantageously suitable for these pulses.
- 6 In a further preferred embodiment of the method the actuator is
- 7 not controlled using gaps, i.e. the amplitude of the current
- 8 continues to rise without pausing after a predefined minimum
- 9 has been reached.
- 10 The invention is explained in more detail below on the basis of
- 11 exemplary embodiments with reference to the schematic drawings.
- 12 The figures show:
- 13 Figure 1 an exemplary embodiment of a curve of the current fed
- 14 to an actuator and the resulting charging of the actuator over
- 15 time,
- 16 Figure 2 a further exemplary embodiment of a curve for current
- 17 fed to the actuator,
- 18 Figure 3 a block diagram of a device for controlling a
- 19 capacitive actuator,
- 20 Figure 4a a first exemplary embodiment of a control unit, and
- 21 Figure 4b a second exemplary embodiment of a control unit.
- 22 Figures 1 and 2 show the curve for a current I fed to an
- 23 actuator. The amount of charge Q stored by the current I in the
- 24 actuator is also plotted as a function of the time t in Figure
- 25 1.
- 26 The actuator involved here is a capacitive actuator, especially
- 27 a piezoelectric actuator P, as is used to actuate an injection
- 28 valve. Such injection valves are used in internal combustion
- 29 engines for example.

- 1 The upper part of Figure 1 shows the curve of the current I fed
- 2 to the actuator. In this case triangular current pulses PU with
- 3 a pulse duration Tp adjoin each other. The maximum amplitudes
- 4 Îl to În in this case follow a control curve k.
- 5 The shape of the control curve k corresponds to a trapeze.
- 6 During a first time T1 the maximum amplitudes În of the current
- 7 I rise from a predefined minimum ÎminT1, here 0, to a
- 8 predefined maximum ÎmaxTl. This maximum ÎmaxTl is selected as a
- 9 result of the desired charge Q of the actuator P at the end of
- 10 the charge phase (T1+T2+T3) from a predefined characteristic
- 11 data field. The characteristic data field can for example
- 12 contain the assignment of different parameters of the internal
- 13 combustion engine, such as engine speed and/ or load for
- 14 required volume of fuel and thereby for desired charge Q. This
- 15 characteristic data field can for example be determined
- 16 empirically or can also be computed. The change in travel Δd at
- 17 the actuator P corresponds in this case to the charge Q fed to
- 18 the actuator P. The equation then applies for the charge Q.
- 19 $Q = \int I(dt)$.
- 20 The timing of the charge Q stored in the actuator P over the
- 21 time t is plotted in the lower part of Figure 1. During the
- 22 first time T1 the amount of charge Q stored in the actuator P
- 23 rises in proportion to t2.
- 24 During a second period of time T2 the maximum amplitude of the
- 25 current pulse PU remains constant. Current pulses PU with a
- 26 maximum amplitude ÎT2 and a pulse width Tp follow on directly
- 27 from one another. Here ÎT2 approximately corresponds to the
- 28 maximum current îmaxT1 of the period T1. During this period T2
- 29 the amount of charge Q fed to the actuator P rises in
- 30 proportion to the time t.

- 1 In the last section T3 the amplitude În of the current pulses
- 2 PU reduces from a predefined maximum îmaxT3 to an also
- 3 predefined final value îminT3, here 0. Here îmaxT3
- 4 approximately corresponds to the maximum amplitude ÎmaxT1 which
- 5 occurs in the period T1. The amount of current Q fed to the
- 6 actuator P behaves during this period T3 in proportion to $(t_3-$
- $7 t)^{2}$.
- 8 The periods T1 and T3 are selected here so that a sufficient
- 9 number of pulses PU are present in T1 or T3. Accordingly a
- 10 switching frequency of $\int p = \frac{1}{2Tp}$ is selected.
- 11 Preferably around 5 to 10 pulses should occur within the rising
- 12 or the falling edge of the curve. The switching frequency ft of
- 13 a final stage E controlling the actuator must be selected
- 14 accordingly. By suitable selection of the pulse width Tp, a
- 15 sufficient averaging of the quantizing induced by the pulse
- 16 width Tp is made possible and the amount of charge is
- 17 controlled in a linear manner over the entire charge time
- 18 T1+T2+T3.
- 19 The pulse width Tp can remain constant during the period T1, T2
- 20 and T3 of the control.
- 21 To achieve a linear control of the travel change Δd at actuator
- 22 P, the amount of charge fed to the actuator is primarily
- 23 achieved by changing the second period T2. In this case the
- 24 falling ramp which forms the envelope curve of the amplitudes Î
- 25 during the third period T3 is shifted in time, the third period
- 26 T3 remains unchanged.
- 27 An alternative embodiment of the current I controlling the
- 28 actuator P is shown in Figure 2. Here the pulse width Tp is
- 29 reduced during the period T3 and thus the switching frequency

- 1 ft increased.
- 2 Figure 3 shows a block diagram of an exemplary embodiment of a
- 3 device for controlling an actuator. The actuator, here a
- 4 piezoelectric actuator P, is connected via an inductor L to a
- 5 final stage E. The final stage E delivers a current I via the
- 6 inductor which charges the piezoelectric actuator. The final
- 7 stage E can be embodied as a conventional switching converter,
- 8 for example as a buck-boost-, flyback or SEPIC converter. The
- 9 final stage E delivers, depending on a control voltage UST
- 10 which is provided by a control unit ST, the current I which
- 11 charges or discharges the piezoelectric actuator P. The
- 12 direction of the current I depicted in Figure 3 shows the
- 13 current direction for a charging process.
- 14 Figure 4a shows a first exemplary embodiment of a control unit
- 15 ST. This unit has a digital-analog converter, preferably a fast
- 16 multiplying digital-analog converter D/A1 with a downstream
- 17 lowpass filter R1', C1'. A predefined value X is fed to the
- 18 digital-analog converter D/A1 at a digital input Din and a
- 19 control voltage UÎmax specifying the maximum amplitude Îmax of
- 20 the current is fed to a further input Ref. The applied control
- 21 voltage UÎmax is then multiplied by the set digital value X and
- 22 output at the output as control voltage UST, so that the
- 23 digital-analog converter operates like a precise digital
- 24 potentiometer. Both the predefined value X and also the maximum
- 25 amplitude are provided by a microcontroller μC . In this case
- 26 the control voltage UÎmax is generated from the digital
- 27 information of the microcontroller μC by a second digital-
- 28 analog converter D/A2. The control signal UST thus generated is
- 29 fed via the lowpass filter formed from the resistor R1' and the
- 30 capacitor C1' to the final stage E. Thus the envelope curve k
- 31 can be predefined without the timing activation of the actuator
- 32 P being changed by the final stage.

- 1 Figure 4b shows an exemplary embodiment of a circuit
- 2 arrangement constructed from analog components for creating the
- 3 control signal UST. The circuit arrangement shown in this
- 4 figure features on one side a charge capacitor C1 connected to
- 5 ground GND, which is connected on the other side via a voltage
- 6 limiter B to the output UST of the circuit arrangement.
- 7 The voltage limiter B is connected at the non-inverting input +
- 8 to a voltage U/2 corresponding to the voltage to be limited.
- 9 The inverting input is connected to the side of the capacitor
- 10 C1 facing away from ground. The output UST of the voltage
- 11 limiter B is also electrically connected to this terminal of
- 12 the capacitor C1. The capacitor C1 is further electrically
- 13 connected via a resistor R5 and a selection switch S1 (for
- 14 switch setting discharge "E") to the supply voltage U. In a
- 15 second switch setting Charge "L" of the switch S1 the capacitor
- 16 C1 is connected via the resistor R5 to the output of an
- 17 operational amplifier OP connected as an inverting voltage
- 18 amplifier. The operational amplifier OP is connected by its
- 19 non-inverting input + to ground GND and by its inverting input
- 20 via a resistor R3 to the supply voltage U halved here by a
- 21 voltage divider R1, R2 (R1=R2). The output of the operational
- 22 amplifier OP is connected via a further resistor R4 back to its
- 23 inverting input.
- 24 Here the ramp of the control signal UST is created by the
- 25 capacitor C1 being charged in switch position L and
- 26 subsequently discharged in switch position E. The function of
- 27 the current limiter B is to limit the discharge current of the
- 28 capacitor C1 so that the control signal UST is located in the
- 29 linear range of the discharge voltage of the capacitor C1.
- 30 Instead of the RC element R5, C1 an ideal integrator can also
- 31 be used however.